

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S1	410853	computer near5 system	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/01/03 15:55
S2	99986	S1 same ((non-volatile adj memory) (nonvolatile adj memory) (non volatile adj memory) rom eprom (flash adj memory) (bubble adj memory) (cmos near3 ram) disc disk (volatile adj memory) RAM)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/01/03 16:03
S3	486	S2 same (((sequential serial subsequent consecutive successional successive succeeding succedent) adj access) ((sequential\$2 serial\$2 subsequent\$2 consecutive\$2 successional\$2 successive\$2 succeeding\$2 succedent\$2) near5 access\$3))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/01/05 10:33
S4	45	S3 same ((Dram sdram memory) near5 interfac\$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/01/03 16:51
S5	411178	computer near5 system	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/01/05 10:33
S6	100104	S5 same ((non-volatile adj memory) (nonvolatile adj memory) (non volatile adj memory) rom eprom (flash adj memory) (bubble adj memory) (cmos near3 ram) disc disk (volatile adj memory) RAM)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/01/05 10:35
S7	1018	S6 same (((sequential serial subsequent consecutive successional successive succeeding succedent) adj access) ((sequential\$2 serial\$2 subsequent\$2 consecutive\$2 successional\$2 successive\$2 succeeding\$2 succedent\$2) with access\$3))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/01/05 10:34

S8	572	S6 same ((initializ\$3 with boot\$3 (boot near5 code))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/01/05 10:37
S9	3	S7 same S8	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/01/05 10:44
S10	29	S7 and S8	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/01/05 10:46
S11	0	S9 same ((4volatile near5 memory) same interface)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/01/05 10:48
S12	2731	S6 same ((4volatile near5 memory) same interface)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/01/05 10:48
S13	10	S10 and ((4volatile near5 memory) same interface)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/01/05 10:49
S14	12	S9 S13	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/01/05 13:33
S15	2	S14 and (SDRAM)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/27 17:49

S16	8	S14 and (initializ\$3 same (boot\$3))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/01/05 13:41
S17	4	S14 and ((memory near5 location) same (control signal))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/01/05 14:39
S18	2	S14 and (read\$3 near5 (boot adj code))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/01/05 16:00
S19	2	"I10" and (read\$3 near5 (boot adj code))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/01/05 16:32
S20	410853	computer near5 system	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/01/05 16:32
S21	99986	S20 same ((non-volatile adj memory) (nonvolatile adj memory) (non volatile adj memory) rom eprom (flash adj memory) (bubble adj memory) (cmos near3 ram) disc disk (volatile adj memory) RAM)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/01/05 16:02
S22	1015	S21 same (((sequential serial subsequent consecutive successional successive succeeding succedent) adj access) ((sequential\$2 serial\$2 subsequent\$2 consecutive\$2 successional\$2 successive\$2 succeeding\$2 succedent\$2) with access\$3))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/01/05 16:33
S23	572	S21 same ((initializ\$3 with boot\$3) (boot near5 code))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/01/05 16:34

S24	3	S22 same S23	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/01/05 16:02
S25	29	S22 and S23	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/01/05 16:02
S26	10	S25 and ((4volatile near5 memory) same interface)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/01/05 16:34
S27	12	S24 S26	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/01/05 16:02
S28	8	S27 and (initializ\$3 same (boot\$3))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/01/05 16:35
S29	8	S28 and (control address)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/01/05 16:35
S30	2	S28 and ((control address) near3 line)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/27 17:53
S31	0	S30 and (configur\$3 with ((interface bus) near SDRAM))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/01/05 16:36

S32	0	S30 and (configur\$3 with ((interface bus) near5 SDRAM))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/01/05 16:37
S33	1	S30 and (configur\$3 with ((interface bus)))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/01/05 16:37
S34	10	S25 and (SDRAM near3 interface)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/01/05 16:37
S35	900	(SDRAM near3 interface)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/01/05 16:32
S36	0	"I17" and (read\$3 near5 (boot adj code))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/01/05 16:38
S37	768	S35 and ((computer processor))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/01/05 16:33
S38	35	S35 same (((sequential serial subsequent consecutive successional successive succeeding succedent) adj access) ((sequential\$2 serial\$2 subsequent\$2 consecutive\$2 successional\$2 successive\$2 succeeding\$2 succedent\$2) with access\$3))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/01/05 16:33
S39	2	S35 same ((initializ\$3 with boot\$3) (boot near5 code))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/01/05 16:48

S40	89	S35 and ((4volatile near5 memory) same interface)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/01/05 16:34
S41	55	S35 and (initializ\$3 same (boot\$3))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/01/05 16:35
S42	866	S35 and (control address)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/01/05 16:35
S43	360	S35 and ((control address) near3 line)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/01/05 16:36
S44	9	S35 and (configur\$3 with ((interface bus) near SDRAM))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/01/05 16:49
S45	28	S35 and (configur\$3 with ((interface bus) near5 SDRAM))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/27 17:15
S46	269	S35 and (configur\$3 with ((interface bus)))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/01/05 16:37
S47	900	S35 and (SDRAM near3 interface)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/01/05 16:37

S48	0	"I17" and (read\$3 with (boot adj code))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/01/05 16:38
S49	0	S37 and S38 and S40 and S41 and S42 and S43 and S45 and S46	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/01/05 16:40
S50	120	S37 and S42 and S43 and S46	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/01/05 16:40
S51	0	S38 and S40 and S41 and S45	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/01/05 16:41
S52	4	S38 and S40	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/01/05 16:50
S53	2	S41 and S45	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/01/05 16:50
S54	11	S39 S44	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/01/05 16:42
S55	17	S52 S53 S54	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/01/05 16:43

S56	6	S55 and S50	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/01/05 16:43
S57	448462	computer near5 system	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/27 17:49
S58	111247	S57 same ((non-volatile adj memory) (nonvolatile adj memory) (non volatile adj memory) rom eprom (flash adj memory) (bubble adj memory) (cmos near3 ram) disc disk (volatile adj memory) RAM)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/27 17:49
S59	1089	S58 same (((sequential serial subsequent consecutive successional successive succeeding succedent) adj access) ((sequential\$2 serial\$2 subsequent\$2 consecutive\$2 successional\$2 successive\$2 succeeding\$2 succedent\$2) with access\$3))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/27 17:49
S60	625	S58 same ((initializ\$3 with boot\$3) (boot near5 code))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/27 17:49
S61	4	S59 same S60	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/27 17:49
S62	33	S59 and S60	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/27 17:49
S63	11	S62 and ((4volatile near5 memory) same interface)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/27 17:49

S64	14	S61 S63	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/27 17:49
S65	4	S64 and (SDRAM)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/27 17:49
S66	0	S65 and (SDRAM near5 interface)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/27 17:50
S67	0	S65 and (SDRAM near5 interfac\$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/27 17:50
S68	0	S65 and (SDRAM with interfac\$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/27 17:50
S69	448462	computer near5 system	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/27 17:53
S70	111247	S69 same ((non-volatile adj memory) (nonvolatile adj memory) (non volatile adj memory) rom eprom (flash adj memory) (bubble adj memory) (cmos near3 ram) disc disk (volatile adj memory) RAm)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/27 17:53
S71	1089	S70 same (((sequential serial subsequent consecutive successional successive succeeding succedent) adj access) ((sequential\$2 serial\$2 subsequent\$2 consecutive\$2 successional\$2 successive\$2 succeeding\$2 succedent\$2) with access\$3))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/27 17:53

S72	625	S70 same ((initializ\$3 with boot\$3) (boot near5 code))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/27 17:53
S73	4	S71 same S72	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/27 17:53
S74	33	S71 and S72	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/27 17:53
S75	11	S74 and ((4volatile near5 memory) same interface)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/27 17:53
S76	14	S73 S75	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/27 17:53
S77	10	S76 and (initializ\$3 same (boot\$3))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/27 17:53
S78	2	S77 and ((control address) near3 line)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/28 12:50
S79	0	S78 and (SDRAM near5 interfac\$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/27 17:53

S80	448771	computer near5 system	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/28 12:51
S81	111359	S80 same ((non-volatile adj memory) (nonvolatile adj memory) (non volatile adj memory) rom eprom (flash adj memory) (bubble adj memory) (cmos near3 ram) disc disk (volatile adj memory) RAM)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/28 12:51
S82	1090	S81 same (((sequential serial subsequent consecutive successional successive succeeding succedent) adj access) ((sequential\$2 serial\$2 subsequent\$2 consecutive\$2 successional\$2 successive\$2 succeeding\$2 succedent\$2) with access\$3))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/28 12:51
S83	625	S81 same ((initializ\$3 with boot\$3) (boot near5 code))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/28 12:51
S84	4	S82 same S83	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/28 12:51
S85	33	S82 and S83	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/28 12:51
S86	11	S85 and ((\$4volatile near5 memory) same interface)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/28 12:51
S87	14	S84 S86	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/28 12:51

S88	4	S87 and (SDRAM)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/28 12:51
S89	448771	computer near5 system	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/28 12:51
S90	111359	S89 same ((non-volatile adj memory) (nonvolatile adj memory) (non volatile adj memory) rom eprom (flash adj memory) (bubble adj memory) (cmos near3 ram) disc disk (volatile adj memory) RAm)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/28 12:51
S91	1090	S90 same (((sequential serial subsequent consecutive successional successive succeeding succedent) adj access) ((sequential\$2 serial\$2 subsequent\$2 consecutive\$2 successional\$2 successive\$2 succeeding\$2 succedent\$2) with access\$3))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/28 12:51
S92	625	S90 same ((initialiZ\$3 with boot\$3) (boot near5 code))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/28 12:51
S93	4	S91 same S92	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/28 12:51
S94	33	S91 and S92	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/28 12:51
S95	11	S94 and ((\$4volatile near5 memory) same interface)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/28 12:51

S96	14	S93 S95	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/28 12:51
S97	10	S96 and (initializ\$3 same (boot\$3))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/28 12:51
S98	2	S97 and ((control address) near3 line)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/28 12:51
S99	1	S98 and (configur\$3 with ((interface bus)))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/28 12:51
S10 0	6	S88 S98 S99	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/28 14:44
S10 1	448771	computer near5 system	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/28 14:44
S10 2	111359	S101 same ((non-volatile adj memory) (nonvolatile adj memory) (non volatile adj memory) rom eprom (flash adj memory) (bubble adj memory) (cmos near3 ram) disc disk (volatile adj memory) RAm)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/28 14:44
S10 3	1090	S102 same (((sequential serial subsequent consecutive successional successive succeeding succedent) adj access) ((sequential\$2 serial\$2 subsequent\$2 consecutive\$2 successional\$2 successive\$2 succeeding\$2 succedent\$2) with access\$3))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/28 14:44

S10 4	625	S102 same ((initializ\$3 with boot\$3) (boot near5 code))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/28 14:44
S10 5	4	S103 same S104	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/28 14:44
S10 6	33	S103 and S104	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/28 14:44
S10 7	11	S106 and ((4volatile near5 memory) same interface)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/28 14:44
S10 8	14	S105 S107	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/28 14:44
S10 9	4	S108 and (SDRAM)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/28 14:44
S11 0	448771	computer near5 system	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/28 14:44
S11 1	111359	S110 same ((non-volatile adj memory) (nonvolatile adj memory) (non volatile adj memory) rom eprom (flash adj memory) (bubble adj memory) (cmos near3 ram) disc disk (volatile adj memory) RAM)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/28 14:44

S11 2	1090	S111 same (((sequential serial subsequent consecutive successional successive succeeding succedent) adj access) ((sequential\$2 serial\$2 subsequent\$2 consecutive\$2 successional\$2 successive\$2 succeeding\$2 succedent\$2) with access\$3))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/28 14:44
S11 3	625	S111 same ((initializ\$3 with boot\$3) (boot near5 code))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/28 14:44
S11 4	4	S112 same S113	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/28 14:44
S11 5	33	S112 and S113	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/28 14:44
S11 6	11	S115 and ((4volatile near5 memory) same interface)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/28 14:44
S11 7	14	S114 S116	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/28 14:44
S11 8	10	S117 and (initializ\$3 same (boot\$3))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/28 14:44
S11 9	2	S118 and ((control address) near3 line)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/28 14:44

S12 0	1	S119 and (configur\$3 with ((interface bus)))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/28 14:44
S12 1	6	S109 S119 S120	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/28 14:44
S12 2	0	S121 and (SDRAM near2 style near5 interface)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/28 14:53
S12 3	0	S121 and (SDRAM adj style adj interface)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/28 14:56
S12 4	4	(SDRAM adj style adj interface)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/28 14:59
S12 5	0	S124 same (initializ\$5)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/28 15:05
S12 6	6	S121 and initializ\$5	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/28 17:59
S12 7	2	S126 and (interface near5 initializ\$5)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/28 15:14

S12 8	4	S124 and ((initializ\$5)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/28 15:06
S12 9	0	S124 same ((SDRAM adj style) near5 address)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/28 15:07
S13 0	0	S124 same ((SDRAM) near5 address)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/28 15:09
S13 1	0	S124 same ((SDRAM) with address)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/28 15:13
S13 2	6	S126 S127	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/28 15:13
S13 3	6	S124 S127	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/28 15:14
S13 4	0	S133 and (((least lost) near3 significant) with address)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/28 15:16
S13 5	10	S124 S126	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/28 15:16

S13 6	0	S135 and (((least most) near3 significant) MSB LSB) with address)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/28 15:23
S13 7	9	S135 and (((least most) near3 significant) MSB LSB portion part tag bits) with address)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/28 15:19
S13 8	18670	((((least most) near3 significant) MSB LSB) with address)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/28 15:26
S13 9	195	S138 same ((sequential\$3 successive\$2) near5 (cycle stage operation clock transaction))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/28 15:26
S14 0	108	S138 same ((sequential\$3 successive\$2) near5 (cycle stage clock))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/28 15:26
S14 1	30	((((least most) near3 significant) MSB LSB) near5 address) with ((sequential\$3 successive\$2) near5 (cycle stage clock))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/28 15:27
S14 2	20	((((least most) near3 significant) MSB LSB) near5 address) with ((sequential\$3 successive\$2) near5 (cycle stage))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/28 15:41
S14 3	0	S142 same (SDRAM near5 (address adj line))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/28 15:28

S14 4	0	S141 same (SDRAM near5 (address adj line))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/28 15:28
S14 5	0	S141 same (SDRAM with (address (address adj line)))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/28 15:29
S14 6	2	S141 and (SDRAM with (address (address adj line)))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/28 15:29
S14 7	29	((least near3 significant) near5 address) with ((sequential\$3 successive\$2 serial\$2 subsequent\$2) near5 (cycle stage))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/28 15:51
S14 8	445	((least near3 significant) near5 address) with (sequential\$3 successive\$2 serial\$2 subsequent\$2)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/28 15:45
S14 9	6	S147 same (((most near3 significant) near5 address)) with ((sequential\$3 successive\$2 serial\$2 subsequent\$2) near5 (cycle stage))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/28 15:47
S15 0	16	((least near3 significant) near5 address) with ((occur\$3 present\$3 send\$3 sent transfer\$4) near7 ((sequential\$3 successive\$2 serial\$2 subsequent\$2) near5 (cycle stage))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/28 16:14
S15 1	4	S150 same (((most near3 significant) near5 address)) with ((occur\$3 present\$3 send\$3 sent transfer\$4) near7 ((sequential\$3 successive\$2 serial\$2 subsequent\$2) near5 (cycle stage)))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/28 15:55

S15 2	4	S150 and (((most near3 significant) near5 address)) with ((occur\$3 present\$3 send\$3 sent transfer\$4) near7 ((sequential\$3 succesive\$2 serial\$2 subsequent\$2) near5 (cycle stage)))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/28 15:56
S15 3	6	(((least near3 significant) near5 address) with ((most near3 significant) near5 address)) same ((occur\$3 present\$3 send\$3 sent transfer\$4 read\$3 writ\$4) near7 ((sequential\$3 succesive\$2 serial\$2 subsequent\$2) near5 (cycle stage)))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/28 16:32
S15 4	16	(((least near3 significant) near5 address) with ((most near3 significant) near5 address)) same (((sequential\$3 succesive\$2 serial\$2 subsequent\$2) near5 (cycle stage)))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/28 16:39
S15 5	6	S154 same ((occur\$3 present\$3 send\$3 sent transfer\$4 read\$3 writ\$4 happen\$3) near7 (sequential\$2 serial\$3 successive\$2 subsequent\$2))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/28 17:15
S15 6	34	((SDRAM with (address near3 lines)) same multiplex\$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/28 17:17
S15 7	1	S156 and (((least adj significant) near5 address) same ((most adj significant) near5 address))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/28 17:13
S15 8	1	S157 and (address with (sequential\$2 subsequent\$2 serial\$2 successive\$2))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/28 17:10
S15 9	7	S156 and (((least adj significant) near5 address))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/28 16:55

S16 0	6	S159 and ((most adj significant) near5 address)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/28 16:55
S16 1	6	S160 and (address with (sequential\$2 subsequent\$2 serial\$2 succesive\$2))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/28 16:55
S16 2	22	S156 and (((least adj significant) portion) near5 address) same (((most adj significant) portion) near5 address))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/28 17:09
S16 3	7	S162 and (address with (sequential\$2 subsequent\$2 serial\$2 succesive\$2))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/28 17:11
S16 4	50205	((((least adj significant) portion half 8-bit 16-bit 32-bit) near5 address) same (((most adj significant) portion half 8-bit 16-bit 32-bit) near5 address))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/28 17:15
S16 5	2469	S164 same ((occur\$3 present\$3 send\$3 sent transfer\$4 read\$3 writ\$4 happen\$3) near7 (sequential\$2 serial\$3 successive\$2 subsequent\$2))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/28 17:16
S16 6	0	S165 and (((SDRAM with (address near3 lines)) same multiplex\$3))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/28 17:17
S16 7	4	S165 and (((SDRAM with (address near3 lines))))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/28 17:17

S16 8	3	S167 and multiplex\$3	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/28 17:18
S16 9	4	S124 and (SDRAM style interface initializ\$5 address lines multiplex\$3 switch\$3 least significant most significant portion sequential\$2 serial\$2 subsequent\$2 successive\$2 cycle stage access logic first address sequence second subsequent location read\$3 control signal)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/29 17:07
S17 0	4	(SDRAM adj style adj interface)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/29 17:08
S17 1	34	((SDRAM with (address near3 lines)) same multiplex\$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/29 17:08
S17 2	7	S171 and (((least adj significant) near5 address))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/29 17:08
S17 3	22	S171 and (((((least adj significant) portion) near5 address) same (((most adj significant) portion) near5 address))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/29 17:08
S17 4	7	S173 and (address with (sequential\$2 subsequent\$2 serial\$2 successive\$2))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/29 17:08
S17 5	50205	((((least adj significant) portion half 8-bit 16-bit 32-bit) near5 address) same (((most adj significant) portion half 8-bit 16-bit 32-bit) near5 address))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/29 17:08

S17 6	2469	S175 same ((occur\$3 present\$3 send\$3 sent transfer\$4 read\$3 writ\$4 happen\$3) near7 (sequential\$2 serial\$3 successive\$2 subsequent\$2))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/29 17:08
S17 7	4	S176 and (((SDRAM with (address near3 lines))))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/29 17:08
S17 8	3	S177 and multiplex\$3	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/29 17:08
S17 9	4	S170 and (SDRAM style interface initializ\$5 address lines multiplex\$3 switch\$3 least significant most significant portion sequential\$2 serial\$2 subsequent\$2 successive\$2 cycle stage access logic first address sequence second subsequent location read\$3 control signal)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/29 17:08
S18 0	16	S179 S178 S177 S174 S172	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/29 17:09
S18 1	3	ss and (configur\$5 same (SDRAM near5 interface))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/29 17:12
S18 2	0	S180 and (configur\$5 with (SDRAM near5 interface))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/29 17:12
S18 3	90	(configur\$5 with (SDRAM near5 interface))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/29 17:14

S18 4	16	S183 same ((non-volatile flash \$3ROM) near3 memory)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/29 17:30
S18 5	0	S184 same (initializ\$5 near5 code)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/29 17:19
S18 6	0	S184 and (initializ\$5 near5 code)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/29 17:32
S18 7	3	S184 and (initializ\$5)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/29 17:19
S18 8	0	S184 same (initializ\$5)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/29 17:19
S18 9	35	S183 and ((non-volatile flash \$3ROM) near3 memory)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/29 18:34
S19 0	3	S189 and (initializ\$5 near5 code)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/29 17:32
S19 1	51	S180 S189	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/29 18:35

S19 2	0	S191 and ((non-volatile with (("same" common) near5 interface)) (volatile with(("same" common) near5 interface)))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/29 18:42
S19 3	0	S191 and ((non-volatile with (("same" common) near5 interface)) with volatile)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/29 18:43
S19 4	52	((non-volatile with (("same" common) near5 interface)) with volatile)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/30 11:17
S19 5	1	((non-volatile with (("same" common) near5 interface)) with SDRAM)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/30 12:18
S19 6	52	((non-volatile with (("same" common) near5 interface)) with volatile)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/30 11:18
S19 7	1	S196 with SDRAM	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/30 11:19
S19 8	1	S196 same SDRAM	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/30 11:19
S19 9	5	S196 and SDRAM	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/30 11:25

S20 0	50	non-volatile with interfac\$3 with SDRAM	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/30 11:26
S20 1	0	S200 same (interfac\$3 near5 initializ\$5)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/30 11:27
S20 2	1	S200 and (interfac\$3 near5 initializ\$5)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/30 11:29
S20 3	553	(interfac\$3 with ((prior) before ahead preceding\$2 previous\$3) with initializ\$5)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/30 14:01
S20 4	0	S203 same ((non-volatile \$3ROM) with SDRAM)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/30 11:42
S20 5	0	S203 same ((non-volatile \$3ROM flash nonvolatile (non near2 volatile)) with SDRAM)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/30 11:43
S20 6	0	S203 same ((non-volatile \$3ROM flash nonvolatile (non near2 volatile)) same SDRAM)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/30 11:44
S20 7	1	S203 and ((non-volatile \$3ROM flash nonvolatile (non near2 volatile)) with SDRAM)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/30 11:44

S20 8	7	S203 and ((non-volatile \$3ROM flash nonvolatile (non near2 volatile)) same SDRAM)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/30 12:13
S20 9	30	S203 same (((memory near3 interfac\$3) with ((prior) before ahead preceding\$2 previous\$3) with initializ\$5))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/30 12:16
S21 0	0	S209 and (((((non-volatile \$3ROM flash nonvolatile (non near2 volatile))) with interfac\$3) same (SDRAM with interfac\$3))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/30 12:15
S21 1	0	S203 and (((((non-volatile \$3ROM flash nonvolatile (non near2 volatile))) with interfac\$3) same (SDRAM with interfac\$3))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/30 12:15
S21 2	436	((((non-volatile \$3ROM flash nonvolatile (non near2 volatile))) with interfac\$3) same (SDRAM with interfac\$3))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/30 13:47
S21 3	0	S212 same (((memory near3 interfac\$3) with ((prior) before ahead preceding\$2 previous\$3) with initializ\$5))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/30 12:15
S21 4	0	S212 and (((memory near3 interfac\$3) with ((prior) before ahead preceding\$2 previous\$3) with initializ\$5))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/30 12:15
S21 5	1	S212 and ((non-volatile with ("same" common) near5 interface) with SDRAM)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/30 12:18

S21 6	104	S212 and (("same" common) near5 interfac\$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/30 12:19
S21 7	5	S212 same ("same" common) near5 interfac\$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/30 12:19
S21 8	288	((interfac\$3 near5 initializ\$5) with ((prior) before ahead preceding\$2 previous\$3))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/30 15:06
S21 9	56	S218 and (((least low\$3 high\$3) near7 address) with ((most high\$3 low\$3) near7 address))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/30 14:38
S22 0	11	S218 and (((((least low\$3 high\$3) near7 address) with ((most high\$3 low\$3) near7 address))) same (subsequent\$2 successive\$2 serial\$3 sequential\$2 consecutive\$2))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/30 14:11
S22 1	14	S218 and SDRAM	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/30 14:19
S22 2	9	S219 and (bus with (((address bit) near5 (transfer\$4 transmit\$4 broadcast\$3))) with (successive\$2 subsequent\$2 serial\$3 consecutive\$2)))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/30 14:41
S22 3	9	S219 and (bus with (((address bit) near5 (transfer\$4 transmit\$4 broadcast\$3 send\$3 sent))) with (successive\$2 subsequent\$2 serial\$3 consecutive\$2)))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/30 14:52

S22 4	3	S223 and ((least adj significant) with (most adj significant))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/30 14:53
S22 5	2976	(bus with (((address bit) near5 (transfer\$4 transmit\$4 broadcast\$3 send\$3 sent))) with (successive\$2 subsequent\$2 serial\$3 consecutive\$2)))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/30 14:52
S22 6	52	S225 same ((least adj significant) with (most adj significant))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/30 14:56
S22 7	2	S226 and (address\$3 near5 multiplex\$5)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/30 14:57
S22 8	394	S225 and ((least adj significant) with (most adj significant))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/30 15:08
S22 9	157	S228 and (address\$3 near5 multiplex\$5)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/30 14:57
S23 0	154	S229 and (cycle stage)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/30 14:58
S23 1	18	S230 and SDRAM	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/30 14:58

S23 2	389797	((interfac\$3 near5 initializ\$5) with (((prior) before ahead preceding\$2 previous\$3))) near5 use usage)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/30 15:07
S23 3	186	S232 same SDRAM	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/30 15:47
S23 4	3	S233 and ((least adj significant) with (most adj significant))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/30 15:46
S23 5	3	S233 and (((least adj significant) same (most adj significant)))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/30 15:47
S23 6	38	S233 and (((least adj significant) (most adj significant)))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/30 16:03
S23 7	38	S236 and SDRAM	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/30 15:48
S23 8	13	S237 and (address with multiplex\$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/30 15:48
S23 9	4031	(((least adj significant) with (most adj significant)))) same (successive\$2 subsequent\$2 serial\$3 consecutive\$2)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/30 16:04

S24 0	391	((((least adj significant) with (most adj significant)))) same ((successive\$2 subsequent\$2 serial\$3 consecutive\$2) near5 (cycle clock stage access))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/30 16:05
S24 1	25	S240 same (address near5 (portion part subset high\$3 low\$3))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/01 11:14
S24 2	10	("4760524" "6128731" "6295596" "6226738" "5509137").pn.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/01 11:15
S24 4	0	S242 same ((non-volatile \$3ROM) near5 memory)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/01 13:03
S24 5	0	S242 same ((non-volatile (non adj volatile) \$3ROM) near7 memory)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/01 13:04
S24 6	3	S242 and ((non-volatile (non adj volatile) \$3ROM) near7 memory)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/01 13:05
S24 7	3	S246 and (interfac\$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/01 13:06
S24 8	0	S247 and (sequential\$3 near5 access\$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/01 13:06

S24 9	1	S242 and (sequential\$3 near5 access\$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/01 13:07
S25 0	4	S247 S249	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/01 11:31
S25 1	0	S250 and ((sequential\$3 near5 access\$3) with logic)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/01 13:09
S25 2	52436	((non-volatile \$3ROM) near5 memory) same (SDRAM interfac\$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/01 13:35
S25 3	0	S252 same (sequential\$3 near5 access\$5 with logic)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/01 11:38
S25 4	809610	S252 ad (sequential\$3 near5 access\$5 with logic)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/01 11:38
S25 5	39	S252 and (sequential\$3 near5 access\$5 with logic)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/01 11:41
S25 6	28	S252 same (((sequential\$3 serial\$3 consecutive\$2 subsequent\$2) near5 access\$3) with logic)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/01 11:40

S25 7	9	S255 and ((first near3 (portion address sequence location)) with (second near3 (address sequence portion location)))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/01 11:47
S25 8	3	S257 and (read\$3 with (control near3 (signal pulse clock)))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/01 11:44
S25 9	2	S255 and ((first near3 (address)) with (second near3 (address)))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/01 12:12
S26 0	4436	(((first near3 (address)) with (second near3 (address)))) same (subsequent\$2 serial\$3 sequential\$3 successive\$2 consecutive\$2)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/01 12:00
S26 1	179	S260 same (read\$3 with (control near5 signal))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/01 12:09
S26 2	1	S261 same (sequential\$3 with access\$5 with logic)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/01 12:03
S26 3	6	S261 and (sequential\$3 with access\$5 with logic)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/01 12:09
S26 4	7	(sequential\$3 adj access\$5 adj logic)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/01 12:13

S26 5	0	S264 and (read\$3 with (control near5 signal))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/01 12:10
S26 6	0	S264 and (read\$3 same (control near5 signal))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/01 12:10
S26 7	6	S264 and (r (control near5 signal))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/01 12:10
S26 8	5	S264 and ((control near5 signal))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/01 12:12
S26 9	0	S268 and (read\$3 near5 (address location))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/01 12:11
S27 0	0	S268 and (read\$3 with (address location))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/01 12:11
S27 1	0	S268 and ((first near7 (address)) same (second near7 (address)))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/01 12:12
S27 2	1	S249 and (address)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/01 12:15

S27 3	2	"6748457".pn.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/01 13:02
S27 4	12	S242 S273	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/01 13:02
S27 5	0	S273 same ((non-volatile \$3ROM) near5 memory)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/01 13:03
S27 6	0	S273 same ((non-volatile (non adj volatile) \$3ROM) near7 memory)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/01 13:04
S27 7	2	S273 and ((non-volatile (non adj volatile) \$3ROM) near7 memory)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/01 13:05
S27 8	2	S277 and (interfac\$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/01 13:06
S27 9	1	S278 and (sequential\$3 near5 access\$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/01 13:06
S28 0	1	S279 and (sequential\$3 near5 access\$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/01 13:25

S28 1	0	S280 and ((sequential\$3 near5 access\$3) with logic)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/01 13:09
S28 2	0	S280 and ((first near3 address) same ((second next subsequent) near3 address))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/01 13:26
S28 3	0	S280 and ((first near3 address))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/01 13:27
S28 4	1	S280 and ((second next subsequent) near3 address)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/01 13:27
S28 5	1138	S252 and (sequential\$3 near5 access\$5)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/01 14:07
S28 6	55	S252 same (sequential\$3 near5 access\$5)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/01 13:35
S28 7	5	S286 same ((address location) with (first second next subsequent\$2 successive\$3 serial\$3 sequen\$6))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/01 13:40
S28 8	31	S286 and ((address location) with (first second next subsequent\$2 successive\$3 serial\$3 sequen\$6))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/01 14:06

S28 9	845	S285 and ((address location) with (first second next subsequent\$2 successive\$3 serial\$3 sequen\$6))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/01 14:08
S29 0	55	S252 same (sequential\$3 near5 access\$5)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/01 14:07
S29 1	31	S290 and ((address location) with (first second next subsequent\$2 successive\$3 serial\$3 sequen\$6))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/01 14:08
S29 2	19	S291 and (volatile SDRAM)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/01 14:10
S29 3	5	S291 and (SDRAM)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/01 14:11
S29 4	5	S293 and (control near3 signal)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/01 14:20
S29 5	12	S242 S273	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/01 14:20
S29 6	6	S295 and ((non-volatile (non adj volatile) \$4ROM) with memory)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/01 14:34

S29 7	1	S296 and (sequential\$2 near5 access\$5)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/01 14:41
S29 8	1109	((first near3 address) with ((second next subsequent) near3 address)) same (read\$3 same (control near5 signal))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/01 14:43
S29 9	0	S298 same (SDRAM with sequential\$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/01 14:44
S30 0	0	S298 same ((SDRAM volatile) with sequential\$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/01 14:45
S30 1	0	S298 same (((SDRAM volatile) with ((sequential\$3 serial\$3 successive\$3 subsequent\$3 (one adj after adj another) consecutive\$2)) near (address location block)))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/01 14:50
S30 2	1	S298 same (((SDRAM volatile) same ((sequential\$3 serial\$3 successive\$3 subsequent\$3 (one adj after adj another) consecutive\$2)) near (address location block)))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/01 15:45
S30 3	13	S295 S302	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/01 15:45
S30 4	3	S303 and (initializ\$5 with boot\$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/01 15:54

S30 5	1	S303 and ((initializ\$5 with boot\$3) same read\$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/01 15:52
S30 6	3	S304 and (\$4ROM non-volatile (non adj volatile))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/01 16:26
S30 7	2	S306 and (first near5 (memory location address block segment))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/01 16:28
S30 8	1	S307 and ((non-volatile (non adj volatile) \$4ROM) with read\$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/01 16:10
S30 9	1	S308 and (control near5 signal)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/01 16:29
S31 0	8	S303 and (\$4ROM non-volatile (non adj volatile))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/01 16:26
S31 1	5	S310 and (first near5 (memory location address block segment))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/01 16:28
S31 2	3	S311 and (control near5 signal)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/01 16:45

S31 3	7	S303 and ((sequential\$3 subsequent\$3 successive\$2 consecutive\$3 serial\$3) near5 (access\$5 read\$4 writ\$5 stor\$5))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/01 16:58
S31 4	4	S313 and (non-volatile \$4ROM flash (non adj volatile))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/02 16:58
S31 5	16	("6748457" "6038648" "6226738" "5509137" "5951685" "4760524" "6128731" "6295596").pn.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/02 17:01
S31 6	7	S315 and ((address location block) with (next consecutive\$2 subsequent\$2 successive\$2 serial\$3))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/02 17:03
S31 7	10	S315 and ((address location block data) with (next consecutive\$2 subsequent\$2 successive\$2 serial\$3))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/02 17:03
S31 8	7	S317 and ((interface bus) with (read\$3 access\$3))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/02 17:05
S31 9	3	S318 and (control near5 signal)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/02 17:08
S32 0	6	S318 and (non-volatile \$4ROM flash (non adj volatile))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/02 17:06

S32 1	3	S320 and (control near5 signal)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/02 17:09
S32 2	3	S321 and (SDRAM volatile \$4RAM)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/02 19:05
S32 3	2	S315 and ((minimiz\$3 reduc\$3 less\$5 diminish\$3 cut\$4) with (address line))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/02 19:19
S32 4	0	S315 and ((minimiz\$3 reduc\$3 less\$5 diminish\$3 cut\$4) with (address near3 line))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/02 19:19
S32 5	3053	((minimiz\$3 reduc\$3 less\$5 diminish\$3 cut\$4) with (address near3 line))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/02 19:20
S32 6	181	S325 same (non-volatile flash \$4ROM)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/02 19:21
S32 7	0	S326 same SDRAM	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/02 19:22
S32 8	1	S326 and SDRAM	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/02 19:22

S32 9	68	S326 same (SDRAM volatile \$4RAM)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/02 19:24
S33 0	56	S329 and (control near5 (line signal))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/02 19:26
S33 1	12	S330 and (reduc\$3 near7 ((control address) near3 (signal line)))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/02 20:05
S33 2	4	S315 and (configur\$3 near5 interface)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/02 20:06
S33 3	4	S332 and (non-volatile \$4ROM flash)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/02 20:07
S33 4	2	S333 and SDRAM	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/02 20:34
S33 5	1	S315 and ((first near5 row) with (access\$3 read\$3 writ\$4 stor\$3))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/02 20:40
S33 6	21	(stor\$3 near5 (initializ\$5 boot BIOS)) same ((first near3 row) near7 (access\$3 read\$3 writ\$4 stor\$3))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/02 21:05

S33 7	10	S336 and (non-volatile (non adj volatile) \$4ROM)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/02 20:43
S33 8	11	S336 and (non-volatile (non adj volatile) flash \$4ROM)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/02 21:22
S33 9	1	("1111111111111111111111111111")	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/02 21:22
S34 0	3	("6516413" "5644636" "5616904").pn.	US-PGPUB; USPAT	OR	ON	2005/07/04 16:21
S34 1	4	("2399668").pn.	USOCR; EPO; DERWENT	OR	ON	2005/07/02 21:26
S34 2	0	S341 and ((protect\$3 near5 data) with (transport\$5 mov\$3 port\$3 handy carry\$3 carri\$3))	USOCR; EPO; DERWENT	OR	ON	2005/07/02 21:35
S34 3	0	S341 and (((protect\$3 guard\$3 keep\$3 preserv\$3 conserv\$3 secur\$3) near5 data) with (transport\$5 mov\$3 port\$3 handy carry\$3 carri\$3))	USOCR; EPO; DERWENT	OR	ON	2005/07/02 21:36
S34 4	0	S341 and (((protect\$3 guard\$3 keep\$3 preserv\$3 conserv\$3 secur\$3) near5 data) same (transport\$5 mov\$3 port\$3 handy carry\$3 carri\$3))	USOCR; EPO; DERWENT	OR	ON	2005/07/02 21:37
S34 5	3584	(((protect\$3 guard\$3 keep\$3 preserv\$3 conserv\$3 secur\$3) near5 data) same (transport\$5 mov\$3 port\$3 handy carry\$3 carri\$3))	USOCR; EPO; DERWENT	OR	ON	2005/07/02 21:37
S34 6	0	S345 same ((protected adj area) with semiconductor)	USOCR; EPO; DERWENT	OR	ON	2005/07/02 21:38
S34 7	0	S345 and((protected adj area) with semiconductor)	USOCR; EPO; DERWENT	OR	ON	2005/07/02 21:39
S34 8	30	S345 and ((protect\$3 secure safe (tamper near2 proof) tamper) with semiconductor)	USOCR; EPO; DERWENT	OR	ON	2005/07/02 21:40

S34 9	0	S348 and ((card memory board) near5 (read/write))	USOCR; EPO; DERWENT	OR	ON	2005/07/02 21:41
S35 0	4	S348 and ((card memory board) near5 ((read/write) read\$3 writ\$3))	USOCR; EPO; DERWENT	OR	ON	2005/07/02 21:46
S35 1	0	S350 and (challenge near3 value)	USOCR; EPO; DERWENT	OR	ON	2005/07/02 21:43
S35 2	1	S350 and ((challenge near3 value) lock key)	USOCR; EPO; DERWENT	OR	ON	2005/07/02 21:44
S35 3	0	S352 and ((compar\$3 calculat\$3 match\$3 correspond\$3) near7 (value key lock))	USOCR; EPO; DERWENT	OR	ON	2005/07/02 21:49
S35 4	0	S352 and ((compar\$3 calculat\$3 match\$3 correspond\$3) with (value key lock))	USOCR; EPO; DERWENT	OR	ON	2005/07/02 21:53
S35 5	4	S350 S352	USOCR; EPO; DERWENT	OR	ON	2005/07/02 21:50
S35 6	0	S355 and ((compar\$3 calculat\$3 match\$3 correspond\$3) with (value key lock))	USOCR; EPO; DERWENT	OR	ON	2005/07/02 21:54
S35 7	0	S340 and ((compar\$3 calculat\$3 match\$3 correspond\$3) with (value key lock))	USOCR; EPO; DERWENT	OR	ON	2005/07/02 21:54
S35 8	11	("6119226" "6748457" "6128731" "6038648" "6226738" "5951685" "5125045" "4760524" "20010052038" "5509137" "6295596").pn.	US-PGPUB; USPAT	OR	ON	2005/07/04 16:26
S35 9	3	S358 and (branch\$3 jump\$3)	US-PGPUB; USPAT	OR	ON	2005/07/04 16:26
S36 0	1	S359 and initializ\$3	US-PGPUB; USPAT	OR	ON	2005/07/04 16:35
S36 1	1	S360 and (program adj space)	US-PGPUB; USPAT	OR	ON	2005/07/04 16:50
S36 2	5	S358 and (((copy\$3 copied writ\$4 stor\$3) with (boot initializ\$5 BIOS)) same (random \$4RAM volatile))	US-PGPUB; USPAT	OR	ON	2005/07/04 16:57
S36 3	1	S362 and (branch\$3 jump\$3)	US-PGPUB; USPAT	OR	ON	2005/07/04 16:55
S36 4	5	S358 and (((copy\$3 copied writ\$4 stor\$3) with (boot initializ\$5 BIOS)) same (random\$5 \$4RAM volatile))	US-PGPUB; USPAT	OR	ON	2005/07/04 17:14
S36 5	1	S364 and (branch\$3 jump\$3)	US-PGPUB; USPAT	OR	ON	2005/07/04 17:15

S36 6	3	S358 and (branch\$3 jump\$3)	US-PGPUB; USPAT	OR	ON	2005/07/04 17:21
S36 7	2	S366 and (boot initializ\$5 BIOS)	US-PGPUB; USPAT	OR	ON	2005/07/04 17:57
S36 8	9	S358 and ((flash adj memory) \$4ROM)	US-PGPUB; USPAT	OR	ON	2005/07/04 17:58
S36 9	5	S358 and ((flash adj memory))	US-PGPUB; USPAT	OR	ON	2005/07/04 18:00